

MRV560 V4.6.0.0 Update Notes

V1.0.0	2018-04-23	• First release
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Supported chips and functions of current version:

Driver chips

MBI series: MBI502x, MBI503x, MBI5041(B), MBI5042(B),

MBI5043, MBI505x, MBI5124 (excluding

MBI5124DPWM), MBI5125 (excluding

MBI5125DPWM), MBI515x, MBI5252

SUM series: SUM20167, SUM2017(T), SUM2028, SUM203x,

SUM213x;

MY series: MY9266, MY9269, MY9366, MY9862, MY9868;

ICN series: ICN2027, ICN2028, ICN2038, ICN2038S;

SM series: SM16158, SM16159, SM16207, SM16227, SM16237;

Others: TLC5958, TLC59581, SC8060, common chips.

Decoding chips

1. 74HC138 chip;
2. 74HC595 chip;
3. RT5953 and RT5958 chips;

Updates

1. Increased the loading capacity to 256×256 pixels;
2. Removed the virtual mode;
3. Optimized the loading capacity in 3D mode. The PWM IC can load 256×256 pixels, and common IC (including dual latch IC) can load 192×256 pixels;
4. Added support for MY9868A, MY9862A, ICN2038A, ICN2038s, SM16207, SM16227 and SM16237 dual latch ICs;
5. Added support for RT5958 decoding chip;
6. Added support for the new Gamma algorithm of SUM2033 chip;
7. Added support for self-test button sharing by two receiving cards in the same cabinet;
8. Added the function of eliminating display of random image immediately after the power is supplied.
9. In the serial mode and each receiving card driving a cabinet of the same width, when the data group mode is changed among 16-group/32-group/64-group, the screen becomes blurred.
10. Supports the LED error detection function.

Notes

The function of eliminating effects of faulty LEDs supported by the MRV560 must work with NovaLCT V5.0.0 or later.